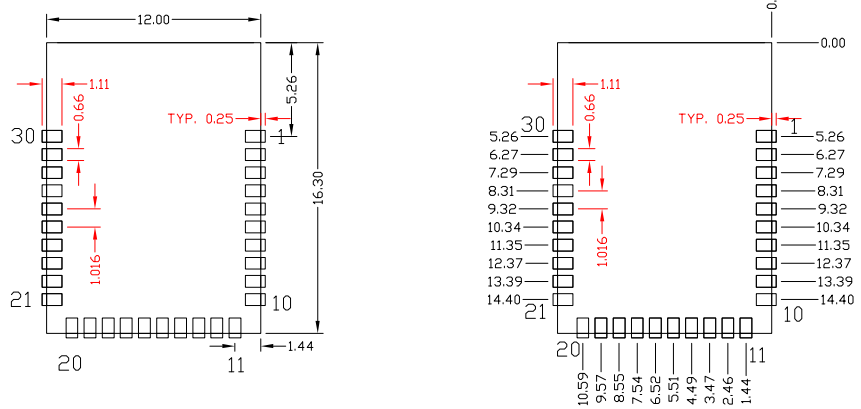
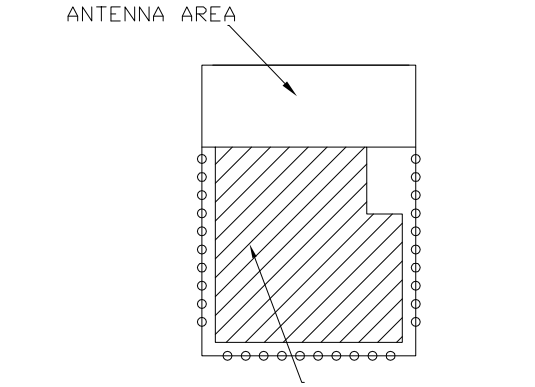
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
0.1	INITIAL RELEASE	WW1811	

RECOMMEND PCB PATTERN

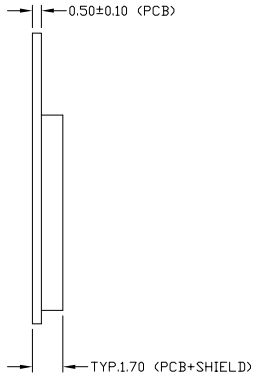


MODULE PAD ASSIGNMENT:

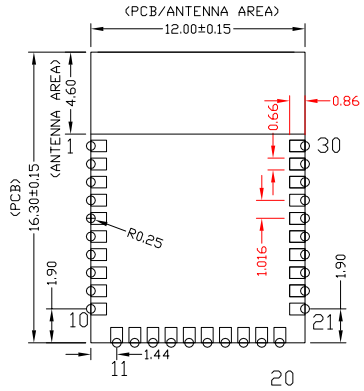
PAD1:GND
PAD2:VCC
PAD3:XRES
PAD4:P25
PAD5:P33
PAD6:P26
PAD7:P38
PAD8:P1
PAD9:P0
PAD10:P29
PAD11:GND
PAD12:P13/P23/P28
PAD13:P17
PAD14:P7
PAD15:P4
PAD16:P2
PAD17:P16
PAD18:XTALI_32K/P15
PAD19:P6
PAD20:XTALO_32K
PAD21:P10/P11
PAD22:P34/P35/P36
PAD23:UART_CTS_N
PAD24:UART_RTS_N
PAD25:UART_TXD
PAD26:UART_RXD
PAD27:HOST_WAKE
PAD28:GND
PAD29:GND
PAD30:GND



SEEN FROM TOP



SEEN FROM SIDE



SEEN FROM BOTTOM



SCALE 1:1

UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN MM	DRAWN:	WW1811
TOLERANCES ON:	CHECKED	
+/- 0.15 MM	ENGRG	
ANGLES +/-0.5 DEGREE	ISSUED	

CYPRESS SEMICONDUCTOR Corp.

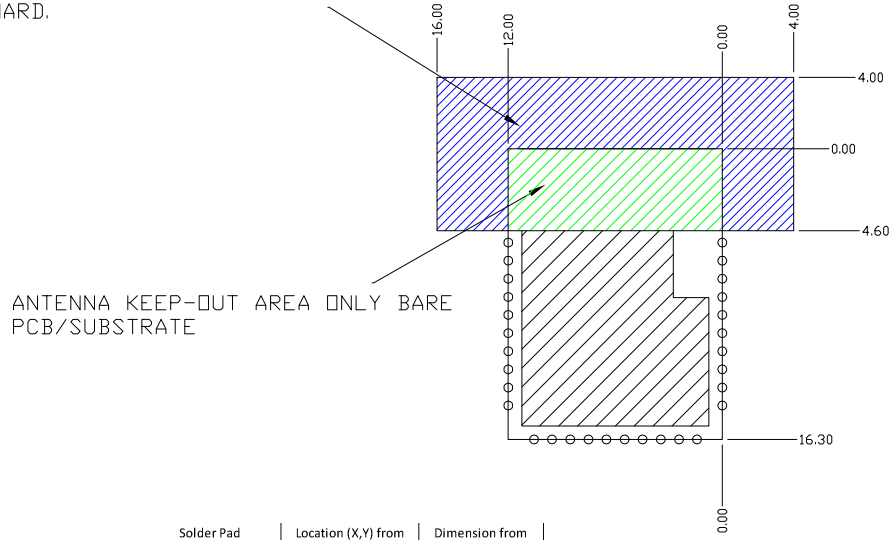
CYBT-413034-02

SIZE	REV NO	DWG NO
	0.1	

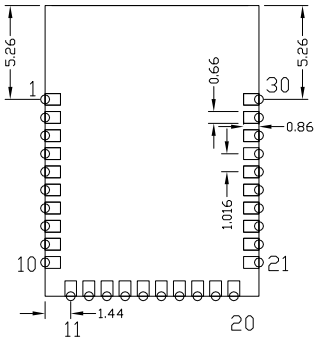
SCALE 5:1 | THE 3RD PROJECTION | SHEET: 1 OF 1

HOST PCB KEEP OUT AREA AROUND ANTENNA

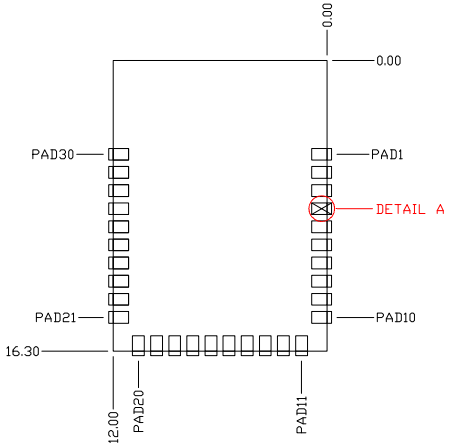
1. FOR BEST RF PERFORMANCE,ADDITIONAL KEEPOUT IN BLUE HATCHED AREA ON THE HOST BOARD ON ALL LAYERS.
2. RECOMMENDATION IS TO PLACE THE BLE MODULE IN THE CORNER OF THE HOST BOARD.

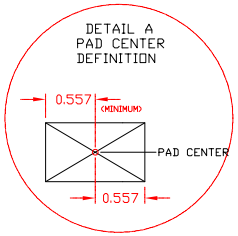


PAD DIMENSION



SOLDER PAD AND LOCATION





Solder Pad (Center of Pad)	Location (X,Y) from Origin (mm)	Dimension from Origin (mil)
1	(0.31, 5.26)	(12.20, 207.09)
2	(0.31, 6.27)	(12.20, 246.85)
3	(0.31, 7.29)	(12.20, 287.01)
4	(0.31, 8.31)	(12.20, 327.16)
5	(0.31, 9.32)	(12.20, 366.93)
6	(0.31, 10.34)	(12.20, 407.09)
7	(0.31, 11.35)	(12.20, 446.85)
8	(0.31, 12.37)	(12.20, 487.01)
9	(0.31, 13.39)	(12.20, 527.16)
10	(0.31,14.40)	(12.20, 566.93)
11	(1.44,15.99)	(56.69, 629.53)
12	(2.46,15.99)	(96.85, 629.53)
13	(3.47,15.99)	(136.61, 629.53)
14	(4.49,15.99)	(176.77, 629.53)
15	(5.51,15.99)	(216.93, 629.53)
16	(6.52,15.99)	(256.69, 629.53)
17	(7.54,15.99)	(296.85, 629.53)
18	(8.55,15.99)	(336.61, 629.53)
19	(9.57,15.99)	(376.77, 629.53)
20	(10.59,15.99)	(416.93, 629.53)
21	(11.69,14.40)	(460.24, 566.93)
22	(11.69,13.39)	(460.24, 527.16)
23	(11.69,12.37)	(460.24, 487.01)
24	(11.69,11.35)	(460.24, 446.85)
25	(11.69,10.34)	(460.24, 407.09)
26	(11.69,9.32)	(460.24, 366.93)
27	(11.69,8.31)	(460.24, 327.16)
28	(11.69,7.29)	(460.24, 287.01)
29	(11.69,6.27)	(460.24, 246.85)
30	(11.69,5.26)	(460.24, 207.09)

UNLESS OTHERWISE SPECIFIED	SIGNATURES	DATE
DIMENSIONS ARE IN MM	DRAWN:	WW1811
TOLERANCES ON:	CHECKED	
+/- 0.20 MM	ENGRG	
ANGLES +/-0.5 DEGREE	ISSUED	

CYPRESS SEMICONDUCTOR Corp.

CYBT-413034-02

SIZE	REV NO	DWG NO
	0.1	

SCALE 5:1 | THE 3RD PROJECTION | SHEET: 2 OF 2